IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):	Michael A. Baxter
PARENT APPLICATION NO.:	09/255,499
PARENT FILING DATE:	February 23, 1999
TITLE:	Meta-Address Architecture for Parallel Dynamically Reconfigurable Computing
EXAMINER:	Larry D. Donaghue
GROUP ART UNIT:	2185
ATTY. DKT. NO.:	20412-08085
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, Washington, D.C. 20231 of the date shown telow: Dated: Dated:	
PRELIMINARY AMENDMENT	
Sir:	examination of the Divisional application identified above, please amend
the specification as follows:	

In the Specification:

Please enter the following paragraph on page 1, line 1 as follows:

[0001] --This application is a divisional application of Serial No. 09/255,499 entitled:
"META-ADDRESS ARCHICHITECTURE FOR PARALLEL, DYNAMICALLY
RECONFIGURABLE COMPUTING", filed on February 23, 1999.--

In the Claims: